



174/295

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Wilson Wong et al.

Application No.: 10/762,864 Confirmation No.: 5928

Filed : January 21, 2004

For : ADAPTIVE EQUALIZATION METHODS AND

APPARATUS FOR PROGRAMMABLE LOGIC

DEVICES

Group Art Unit : 2819

New York, New York 10020

May 13, 2004

Hon. Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

Pursuant to 37 C.F.R. §§ 1.56 and 1.97, applicants hereby make the following references of record in the above-identified patent application:

U.S. Patents

3,633,120	Battjes	01/04/72
4,723,110	Voorman	02/02/88
5,420,538	Brown	05/30/95
6,236,231	Nguyen et al.	05/22/01
6,650,140	Lee et al.	11/18/03
6.724.328	Lui et al.	04/20/04

U.S. Patent Applications

10/317,262	Venkata et`al.	filed 12/10/02
10/317,264	Venkata et al.	filed 12/10/02
10/349,541	Venkata et al.	filed 01/21/03
2003/0052709 A1	Venkata et al.	03/20/03
10/640,824	Wang et al.	filed 08/13/03
10/640,825	Wang et al.	filed 08/13/03
10/702,195	Baig et al.	filed 11/04/03
10/702,196	Bereza et al.	filed 11/04/03
10/756,949	Kwasniewski et al.	filed 01/13/04

Other Documents

B. Gilbert, "The Multi-tanh Principle: A Tutorial Overview", IEEE Journal of Solid-State Circuits, Vol. 33, No. 1, January 1998.

Copies of the aforementioned references, which are listed on the accompanying Form PTO-1449 (submitted in duplicate), are enclosed herewith.

It is respectfully requested that these references be (1) fully considered by the Patent and Trademark Office during examination of this application; and (2) printed on any patent which may issue on this application. Applicants request that a copy of Form PTO-1449, as considered and initialed by the Examiner, be returned with the next communication.

An early and favorable action is respectfully requested.

I hereby certify that this
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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT BY APPLICANTS

ATTY. DOCKET NO. 174/295	APPLICATION NO. 10/762,864		
APPLICANTS Wilson Wong et al.	CONFIRMATION NO. 5928		
FILING DATE January 31, 2004	GROUP ART UNIT 2819		

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	3,633,120	01/04/72	Battjes	330	30R	
	4,723,110	02/02/88	Voorman	330	252	
	5,420,538	05/30/95	Brown	330	252	
	6,236,231	05/22/01	Nguyen et al.	326	39	
	6,650,140	11/18/03	Lee et al.	326	39	
	6,724,328	04/20/04	Lui et al.	341	101	

U.S. PATENT APPLICATIONS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	10/317,262	Ī	Venkata et al.			12/10/02
	10/317,264		Venkata et al.			12/10/02
	10/349,541		Venkata et al.			01/21/03
-	2003/0052709 A1	03/20/03	Venkata et al.	326	37	
- · · · · · · · · · · · · · · · · · · ·	10/640,824		Wang et al.			08/13/03
	10/640,825		Wang et al.			08/13/03
	10/702,195		Baig et al.			11/04/03
	10/702,196		Bereza et al.			11/04/03
	10/756,949		Kwasniewski et al.			01/13/04
	10/757,182		Baig et al.			01/13/04

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL		
	B. Gilbert, "The Multi-Tanh Principle: A Tutorial Overview", IEEE Journal of Solid-State Circuits, Vol. 33, No. 1, January 1998.	_

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicants.